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BN Bershad, S Savage, P Pardyak, EG Sier, ... - ... on Operating systems ..., 1995 - portal.acm.org

... pose network protocol implementations are frequently inadequate for supporting the demands of high performance **parallel** applications [von Eicken et al. 92]. Other ... Co-location. **Operating system** extensions are dynamically linked into the kernel **virtual address** space. ...

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... These "scoreboards" perform the same scheduling **task** at run time that Multiflow's trace scheduling compacting compiler ... The process by which programs are converted into highly **parallel** wide-instruction-word code is ... L Fig. 1. Block diagram of an ideal VLIW **execution** engine. ...

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... The primary disadvantage of **parallel execution** is that the random interleaving (due to load, scheduling, etc.) among the Embra processes makes the simulation non-deterministic. ... **Parallel** Embra is useful for testing and positioning **tasks** which do not require repeatability ...

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... Index Terms-Architecture independence, Mach, **parallel** operating systems, UNIX, **virtual** memory. ...

1) A **task** is an **execution** environment in which threads may run. ... in which the data in the object are accessible copy-on-write, or as the result of a Mach **task**-create operation or a ...

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DL Black, RF Rashid, DB Golub, CR Hill - ACM SIGARCH Computer ..., 1989 - portal.acm.org

... As a result the machine-dependent portion consists of a **single** module, the physical map or pmap module, that implements a simple interface to the memory management hardware. ... **Parallel execution** of a **task** with multiple threads results in the same pmap being used ...

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... and have rich interactions. This article describes Opal, a **single-address-space operating system**
in- tended to support these complex applications on wide-address architectures. Opal provides
a **single global virtual address** space that is shared by all procedures and all data. ...
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... This emulation **task** is likely to be more complex than a complete machine simulation ... In **parallel**
Embra, the scheduler of the host multiprocessor has an impact on the ... to collect both event counts
and timing information describing the simulated machine's **execution** behavior. ...
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DR Engler, MF Kaashoek, J O'Toole Jr - ... on **Operating systems** principles, 1995 - portal.acm.org
... cause there is no **single** way to abstract **physical** resources or to implement an abstraction that
is best for all ... memory library for **parallel** applications ... To perform this **task** efficiently an exokernel
allows library **operating systems** to bind to resources using secure bindings, A secure ...
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... more instructions than individual basic blocks, there is a greater opportunity to find **parallel** work. ...
However, a compiler can perform these **tasks** more efficiently because it has more time, memory ...
blocks by choosing the instructions that are most criti- cal to the **execution** time of the ...
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